

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-15 (cancel)

16. (New) An electronic timepiece comprises:

- a power-source voltage generating unit that converts external energy to electric energy and generates a power source voltage lower than a predetermined operating voltage;

- an oscillation-signal output unit that outputs an oscillation signal when the power source voltage generated is applied to the oscillation-signal output unit;

- a boosting unit that boosts up the power source voltage generated to at least the predetermined operating voltage;

- a boosting control unit that controls the boosting unit to boost up the power source voltage only for a predetermined time after the oscillation signal output unit starts to operate, based upon the oscillation signal; and

- a timing unit that times when the predetermined operating voltage boosted by the boosting control unit is applied to the timing unit.

17. (New) The electronic timepiece according to claim 16, further comprising

- a frequency-dividing unit that frequency-divides the oscillation signal and outputs a frequency-dividing signal; and

- a boosting-stop-instruction-signal output unit that outputs a boosting stop instruction signal, for stopping an boosting behavior performed by the boosting unit, based upon the frequency-dividing signal when the predetermined time elapses after the oscillation-signal output unit outputs the oscillation signal, wherein

- the boosting control unit stops the boosting behavior based upon the boosting stop instruction signal.

18. (New) The electronic timepiece according to claim 16, further comprising a boosting-stop-instruction-signal output unit that outputs a boosting stop instruction signal, for stopping a boosting behavior performed by the boosting unit, when the predetermined time elapses, wherein the boosting behavior is stopped based upon the boosting stop instruction signal.
19. (New) The electronic timepiece according to claim 16, wherein the boosting control unit includes
- a time-information input unit that inputs time information representing one of a plurality of the predetermined times, time intervals of the predetermined times being different from one another;
 - a time-information storage unit that stores the time information inputted;
 - a time-information extracting unit that extracts the time information stored, when the power source voltage generated by the power-source voltage generating unit is applied to the time-information extracting unit; and
 - a boosting-stop-instruction-signal generating unit that generates a boosting stop instruction signal, for stopping a boosting behavior performed by the boosting unit, based upon the time information extracted, when a predetermined time indicated by the time information extracted elapses, wherein the boosting behavior is stopped based upon the boosting stop instruction signal.
20. (New) The electronic timepiece according to claim 16, wherein the oscillation-signal output unit includes
- a constant current circuit that feeds a constant current;
 - an oscillation inverter that reverses and amplifies an input to the oscillation inverter and outputs the oscillation signal, when the constant current circuit supplies a constant current to the oscillation inverter, and has an input terminal and an output terminal;

a resonating circuit that are connected between the input terminal of the oscillation inverter and the output terminal of the oscillation inverter, and resonates.

21. (New) The electronic timepiece according to claim 20, wherein the timing unit includes a logic circuit that has a plurality of field effect transistors, and

the oscillation inverter includes a field effect transistor that has a threshold voltage lower than a threshold voltage of each of the field effect transistors of the timing unit.

22. (New) The electronic timepiece according to claim 20, wherein the timing unit includes a logic circuit that has a plurality of field effect transistors, and

the electronic timepiece further comprises a waveform shaping unit includes a field effect transistor that has a threshold voltage lower than a threshold voltage of each of the field effect transistors of the timing unit, shapes a waveform of the oscillation signal outputted from the oscillation-signal output unit, and outputs an oscillation signal that has the waveform shaped to the timing unit.

23. (New) The electronic timepiece according to claim 20, wherein the timing unit includes a logic circuit that has a plurality of field effect transistors, and

the boosting control unit includes a field effect transistor that has a threshold voltage lower than a threshold voltage of each of the field effect transistors of the timing unit.

24. (New) The electronic timepiece according to claim 20, further comprising a bias circuit that includes a field effect transistor that has a threshold that is same as a threshold of the oscillation inverter, and that applies a predetermined bias voltage to the oscillation inverter.

25. (New) The electronic timepiece according to claim 20, further comprising
a waveform shaping unit that shapes a waveform of the oscillation signal
outputted from the oscillation-signal output unit, and output an oscillation signal that
has the waveform shaped to the timing unit, and

a bias circuit that includes a field effect transistor that has a threshold that is
same as a threshold of the waveform shaping unit, and that applies a predetermined
bias voltage to the waveform shaping unit.

26. (New) An electronic timepiece comprises:

a power-source voltage generating unit that converts external energy to
electric energy and generates a power source voltage lower than a predetermined
operating voltage;

an oscillation-signal output unit that outputs an oscillation signal when the
power source voltage generated is applied to the oscillation-signal output unit;

a boosting unit that boosts up the power source voltage generated to at least
the predetermined operating voltage;

a power-source voltage detecting unit that detects whether the power source
voltage is boosted up to the predetermined operating voltage by the boosting unit;

a boosting control unit that controls the boosting unit, and boosts up the
power source voltage after the oscillation signal output unit starts to operate till the
power source voltage detecting unit detects the predetermined power source
voltage; and

a timing unit that times when the predetermined operating voltage is applied
to the timing unit based on the oscillation.

27. (New) The electronic timepiece according to claim 26, wherein the boosting
control unit controls the boosting behavior performed by the boosting unit based
upon the oscillation signal and the detection result, when a predetermined time
elapses after the oscillation-signal output unit outputs the oscillation signal.

28. (New) The electronic timepiece according to claim 26, wherein the oscillation-signal output unit includes

a constant current circuit that feeds a constant current;

an oscillation inverter that reverses and amplifies an input to the oscillation inverter and outputs the oscillation signal, when the constant current circuit supplies a constant current to the oscillation inverter, and has an input terminal and an output terminal;

a resonating circuit that are connected between the input terminal of the oscillation inverter and the output terminal of the oscillation inverter, and resonates.

29. (New) The electronic timepiece according to claim 28, wherein the timing unit includes a logic circuit that has a plurality of field effect transistors, and

the oscillation inverter includes a field effect transistor that has a threshold voltage lower than a threshold voltage of each of the field effect transistors of the timing unit.

30. (New) The electronic timepiece according to claim 28, wherein the timing unit includes a logic circuit that has a plurality of field effect transistors, and

the electronic timepiece further comprises a waveform shaping unit includes a field effect transistor that has a threshold voltage lower than a threshold voltage of each of the field effect transistors of the timing unit, shapes a waveform of the oscillation signal outputted from the oscillation-signal output unit, and outputs an oscillation signal that has the waveform shaped to the timing unit.

31. (New) The electronic timepiece according to claim 28, wherein the timing unit includes a logic circuit that has a plurality of field effect transistors, and

the boosting control unit includes a field effect transistor that has a threshold voltage lower than a threshold voltage of each of the field effect transistors of the timing unit.

32. (New) The electronic timepiece according to claim 28, further comprising a bias circuit that includes a field effect transistor that has a threshold that is same as a threshold of the oscillation inverter, and that applies a predetermined bias voltage to the oscillation inverter.

33. (New) The electronic timepiece according to claim 28, further comprising a waveform shaping unit that shapes a waveform of the oscillation signal outputted from the oscillation-signal output unit, and output an oscillation signal that has the waveform shaped to the timing unit, and

a bias circuit that includes a field effect transistor that has a threshold that is same as a threshold of the waveform shaping unit, and that applies a predetermined bias voltage to the waveform shaping unit.